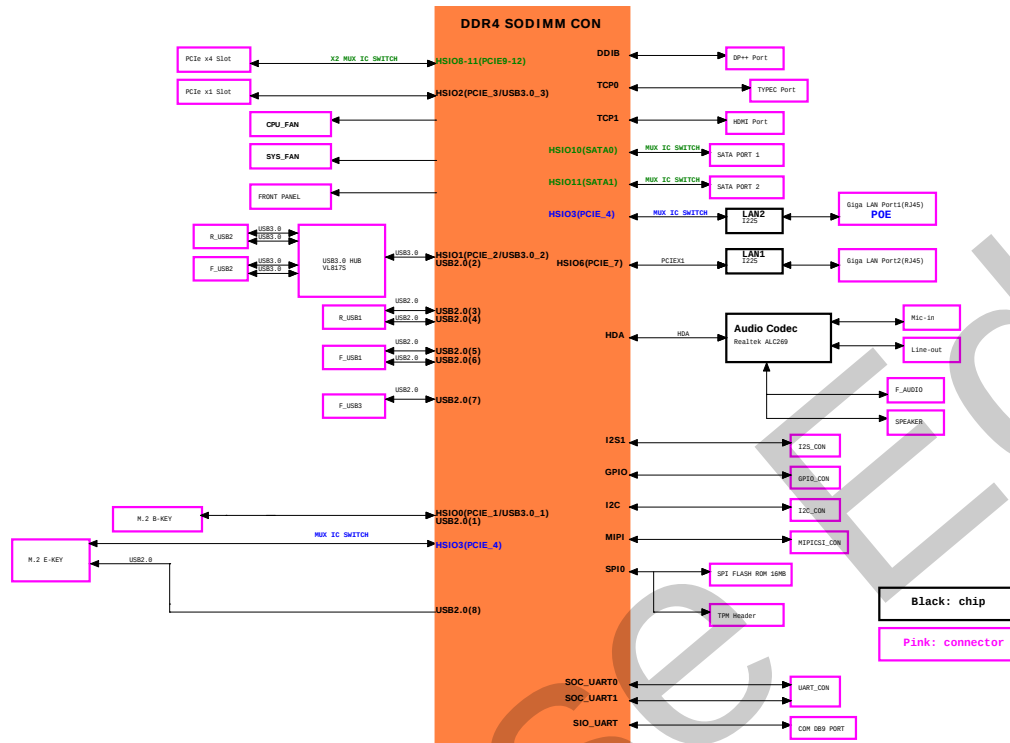
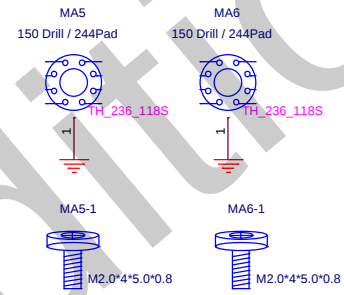
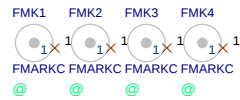
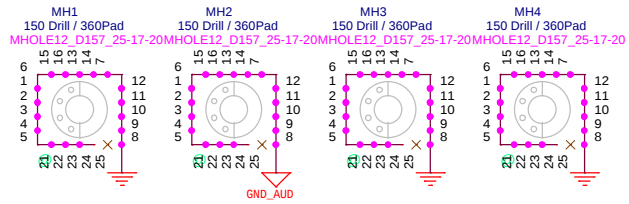


Block Diagram

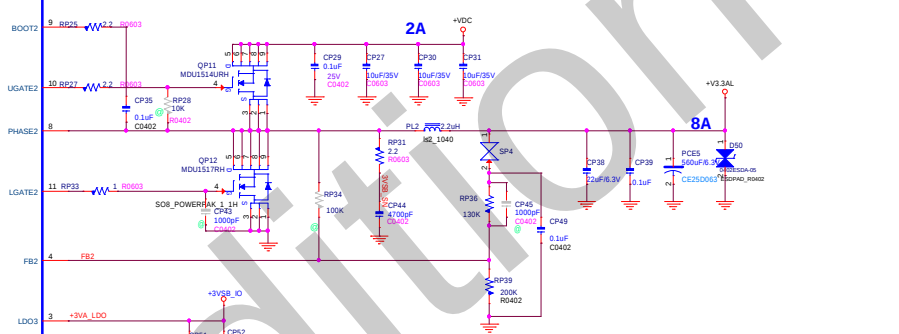
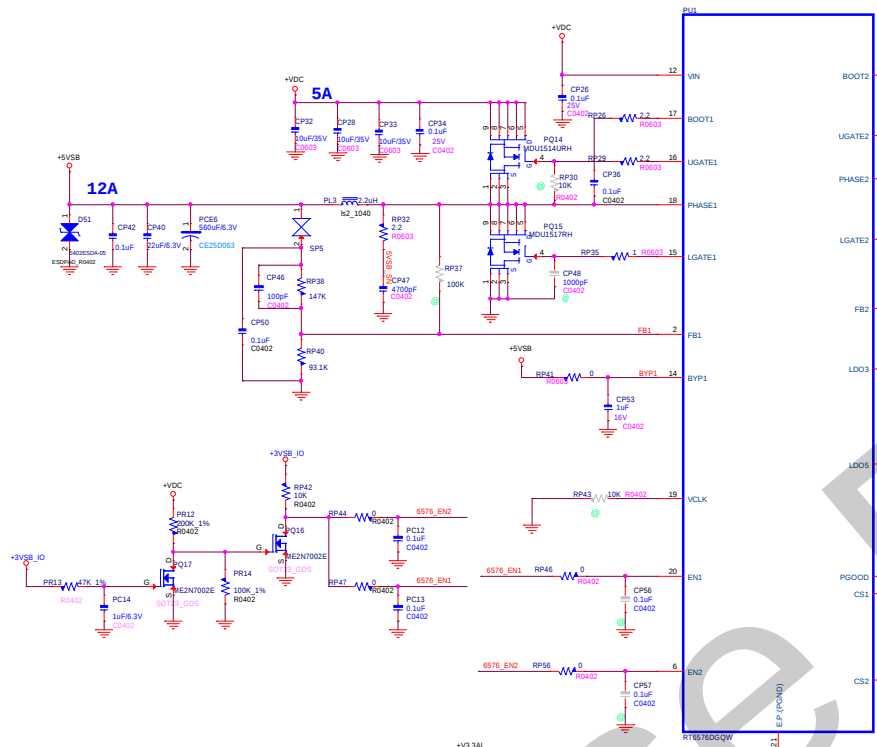


MB HOLE



Crystal

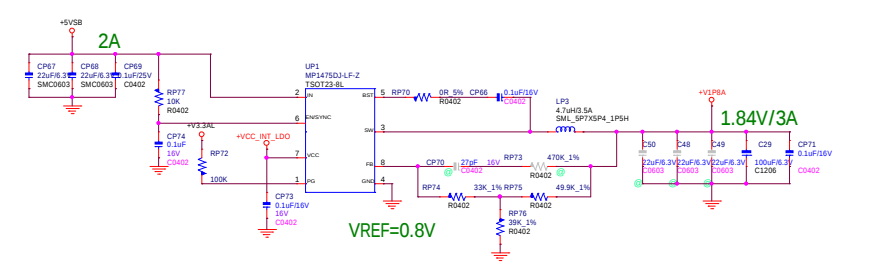
SSRP0+	HC2	0.1uF16VSMC001	HS01_TX_N	2	
SSRP0- <td>HC2</td> <td>0.1uF16VSMC001</td> <td>HS01_TX_N</td> <td>2</td> <td></td>	HC2	0.1uF16VSMC001	HS01_TX_N	2	
SS170+ <td>HC29</td> <td>0.1uF16VSMC001</td> <td>HS01_RX_P</td> <td>2</td> <td></td>	HC29	0.1uF16VSMC001	HS01_RX_P	2	
SS170- <td>HC29</td> <td>0.1uF16VSMC001</td> <td>HS01_RX_N</td> <td>2</td> <td></td>	HC29	0.1uF16VSMC001	HS01_RX_N	2	
USBDP+	HSB2	0.05% 0.05%	USB2_P4	2	
USBDP-	HSB2	0.05% 0.05%	USB2_N4	2	
SS1C3+			USB3_HUB2_TX_DP	14	
SS1C3-			USB3_HUB2_TX_DN	14	
SSRPX3+			USB3_HUB2_RX_DP	14	
SSRPX3-			USB3_HUB2_RX_DN	14	
HS03+			USB2_HUB2_P	14	
HS03-			USB2_HUB2_N	14	
SS1C2+			USB3_HUB1_TX_DP	14	
SS1C2-			USB3_HUB1_TX_DN	14	
SSRPX2+			USB3_HUB1_RX_DP	14	
SSRPX2-			USB3_HUB1_RX_DN	14	
HS02+			USB2_HUB1_P	14	
HS02-			USB2_HUB1_N	14	
SS1X1+			USB3_HUB3_TX_DP	14	
SS1X1-			USB3_HUB3_TX_DN	14	
SSRPX1+			USB3_HUB3_RX_DP	14	
SSRPX1-			USB3_HUB3_RX_DN	14	
HS01+			USB2_HUB3_P	14	
HS01-			USB2_HUB3_N	14	
SS1X4+			USB3_HUB4_TX_DP	14	
SS1X4-			USB3_HUB4_TX_DN	14	
SSRPX4+			USB3_HUB4_RX_DP	14	
SSRPX4-			USB3_HUB4_RX_DN	14	
HS04+			USB2_HUB4_P	14	
HS04-			USB2_HUB4_N	14	



SYS Discharge

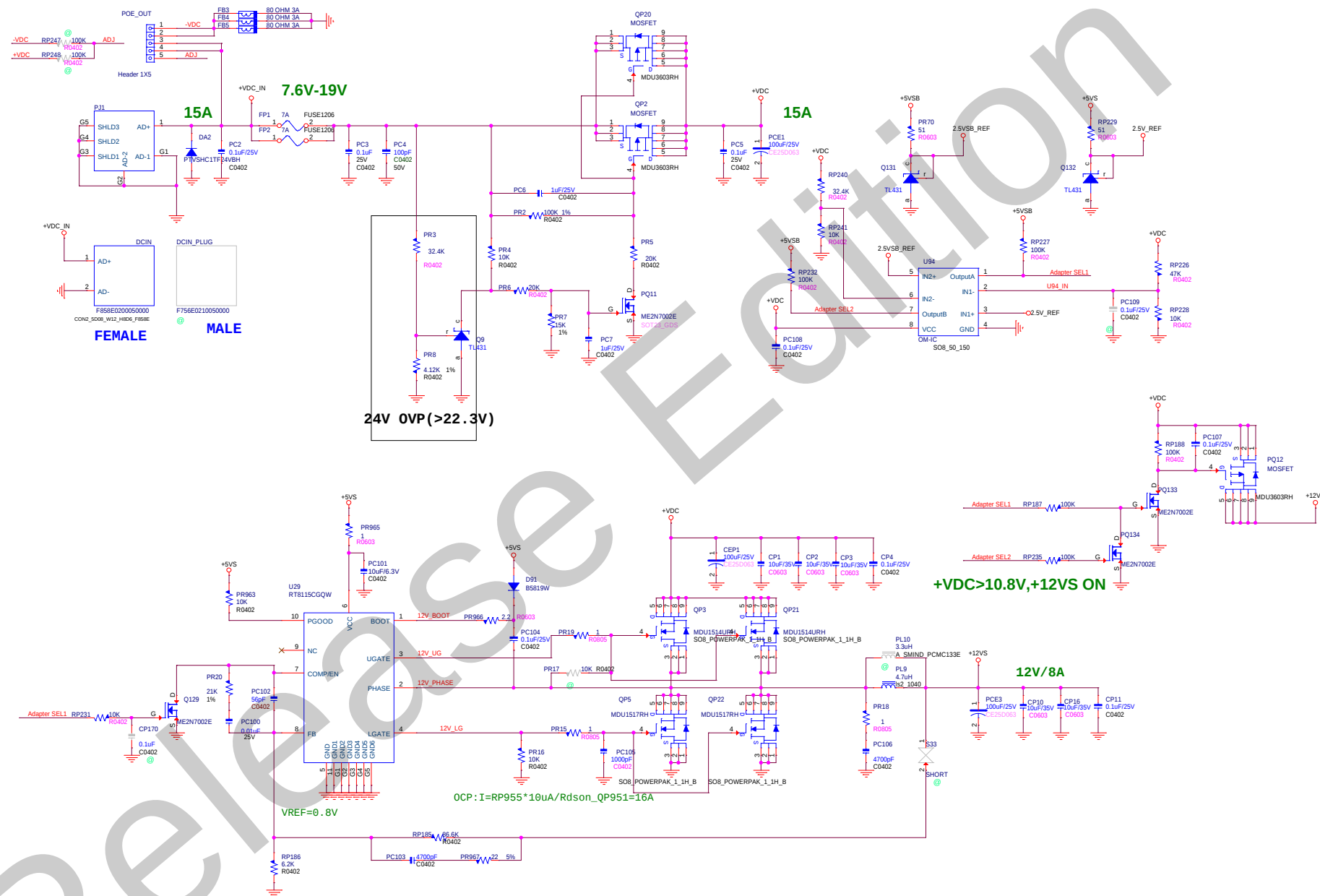
STBY Discharge

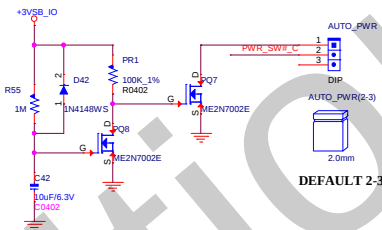
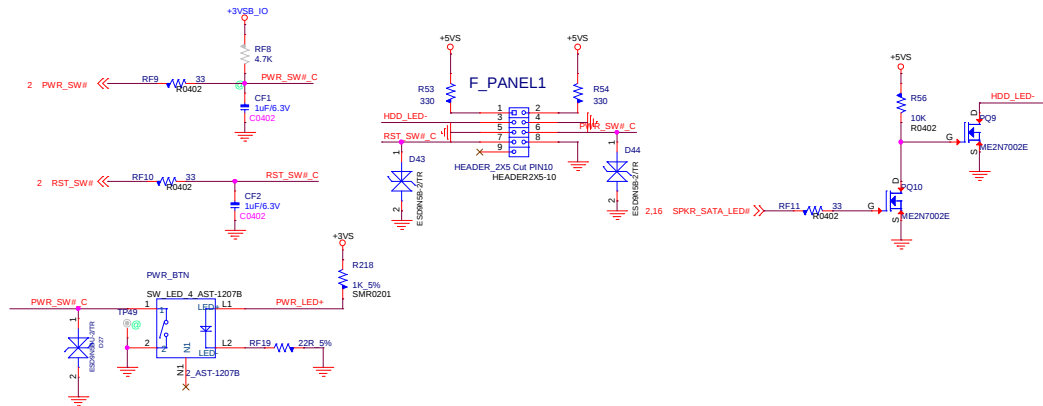
+5VSB<3.2V, QP37 OFF



VREF=0.8V

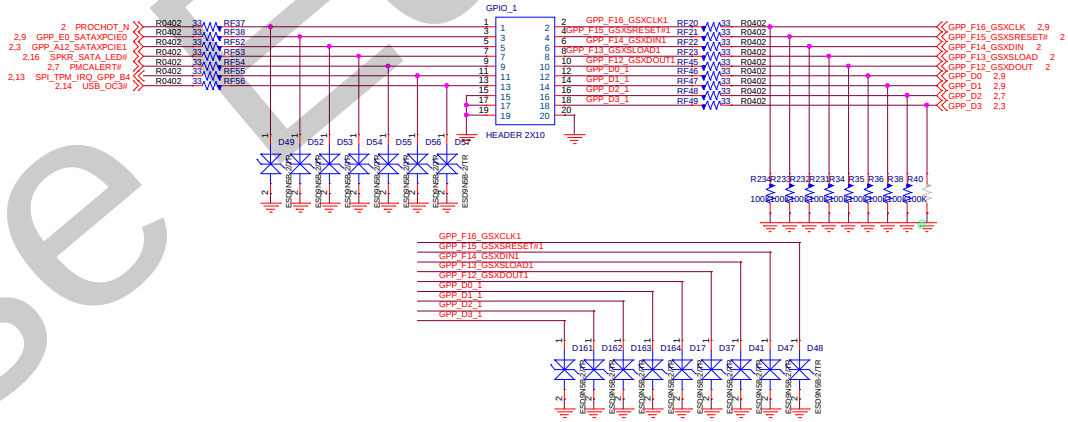
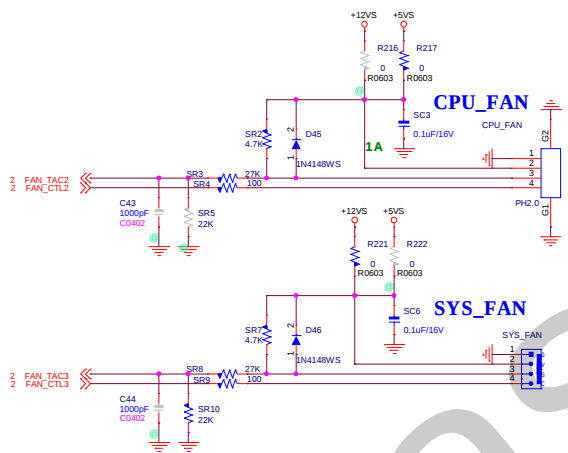
1.84V/3A



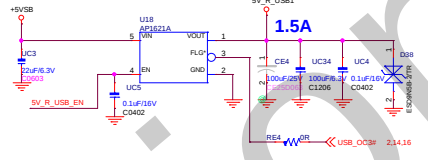
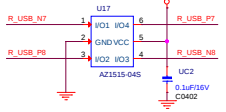
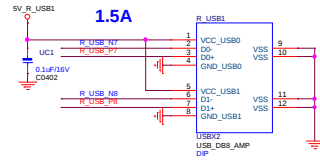
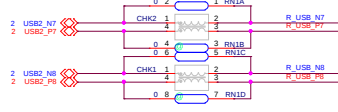


1.8V OR 3.3V
GPIO F12-F16 GSX

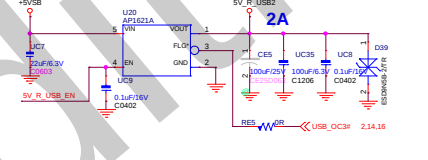
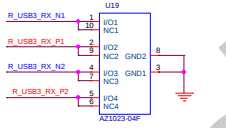
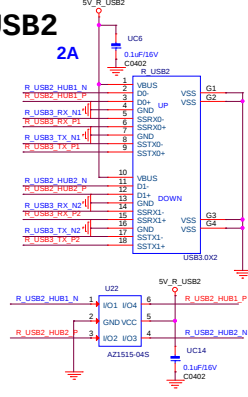
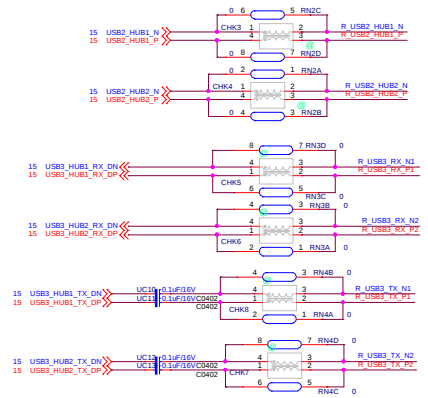
GPIO D0-D3
1.8V OR 3.3V



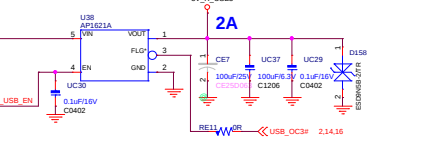
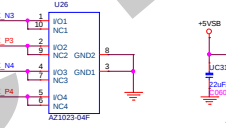
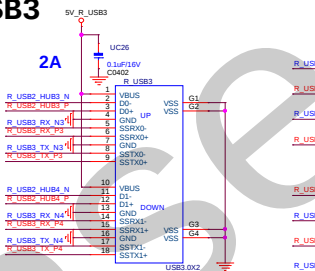
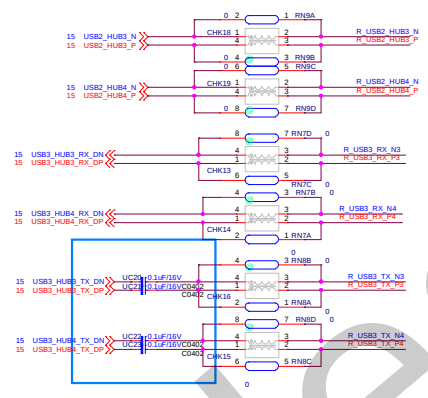
R_USB1



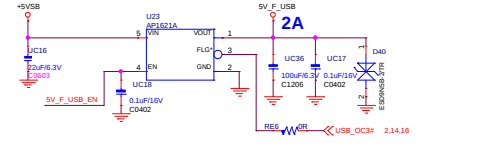
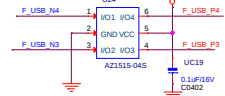
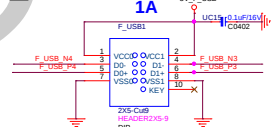
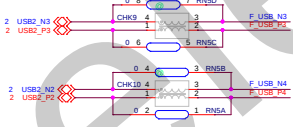
R_USB2



R_USB3

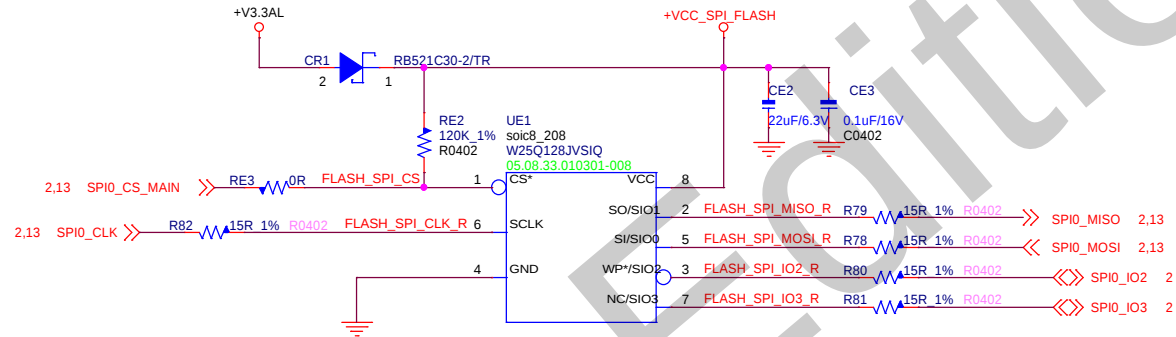


F_USB1

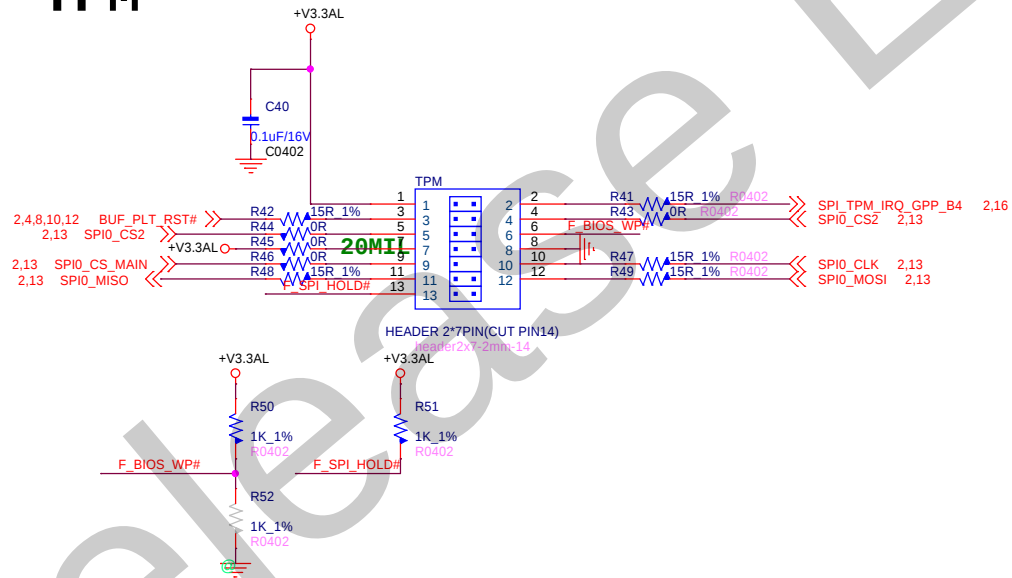


DEFAULT 1-2

MAIN BIOS



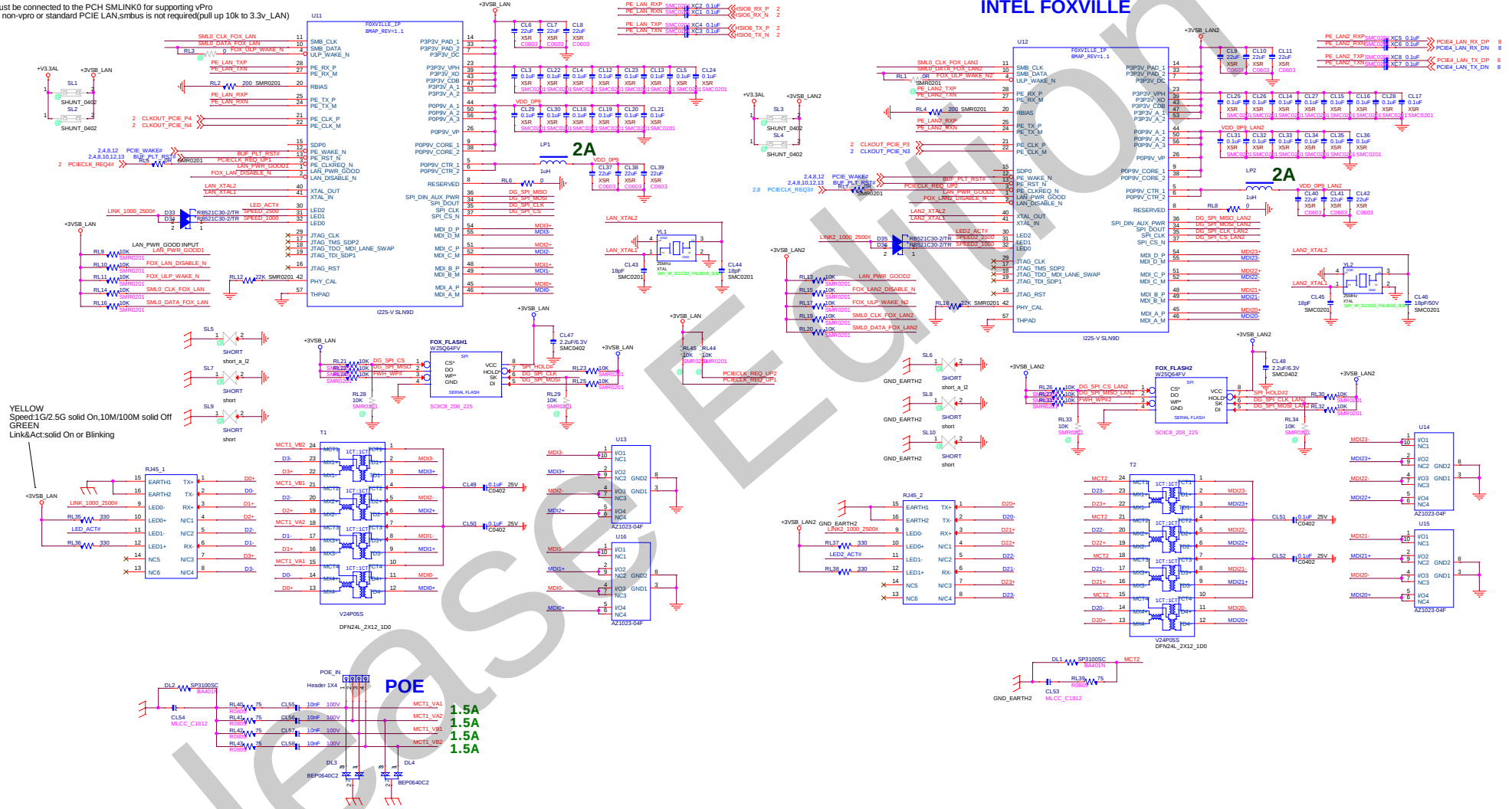
TPM



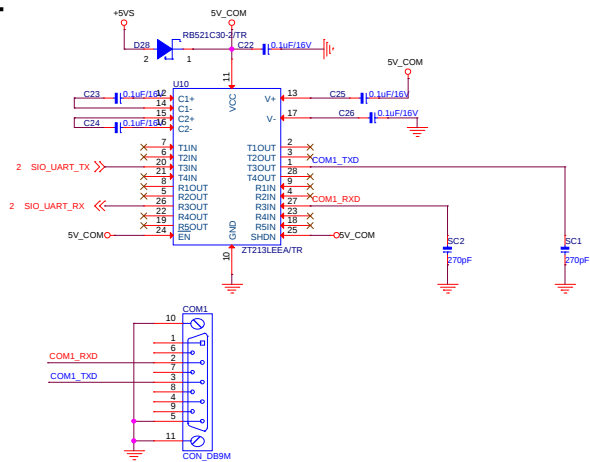
INTEL FOXVILLE

1 must be connected to the PCH SMLINK0 for supporting vPro
2 for non-vpro or standard PCIe LAN, smbus is not required (pull up 10k to 3.3v_LAN)

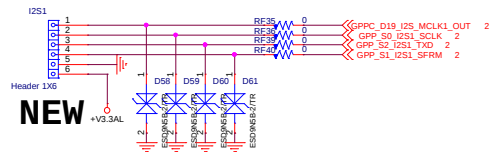
INTEL FOXVILLE



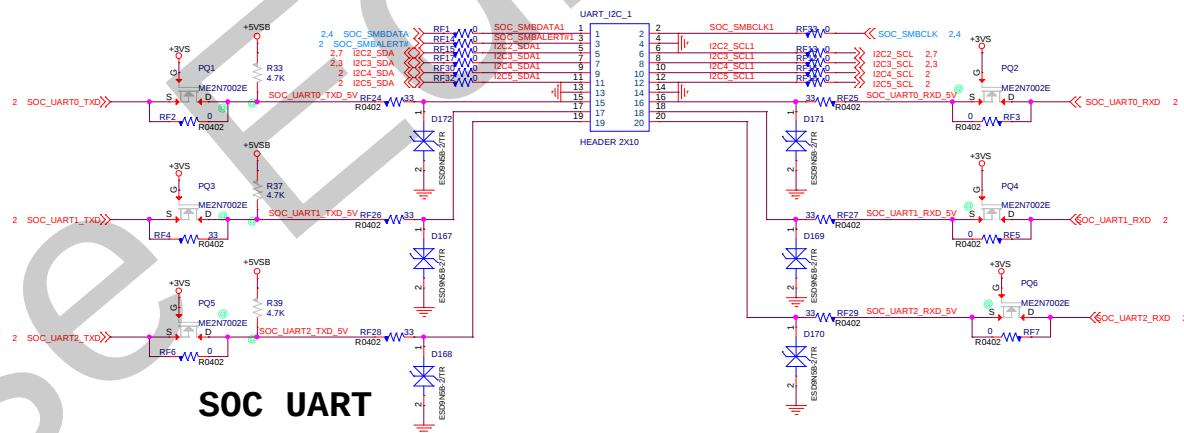
SIO UART



I2S



I2C

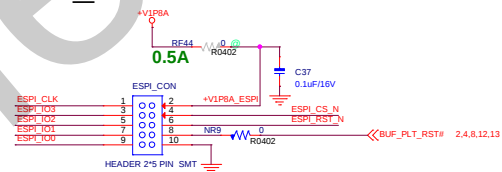


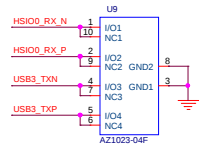
SOC UART

ESPI_POGO



ESPI_HEADER





M.2_BKEY H=4.2

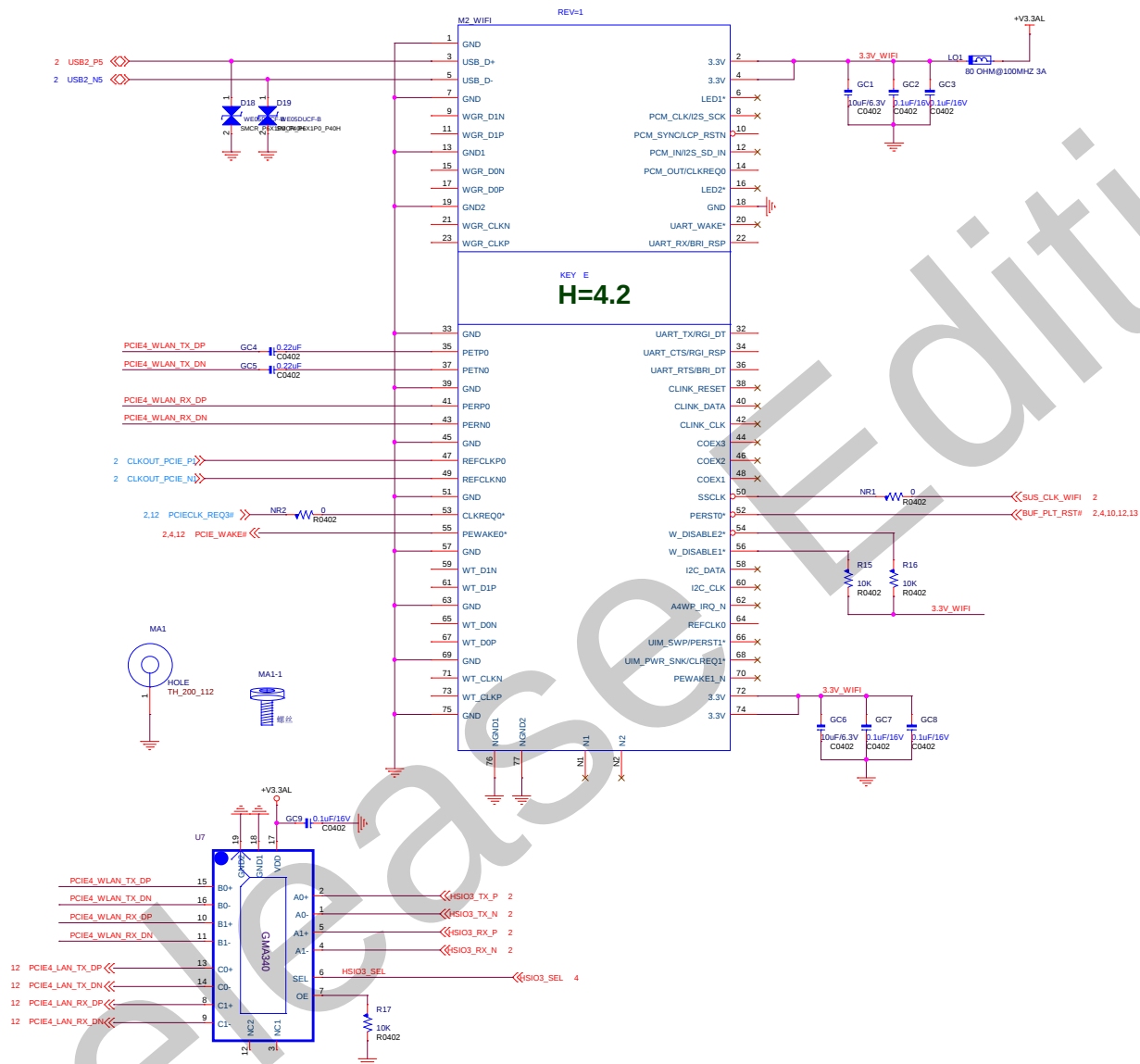
NGFF_SLOT B_KEY B

Presence IND (GND)
GND(3)
3.3Vaux(2)
GND(7)
3.3Vaux(4)
USB_D+
USB_D-
W_DISABLE# (IO)(0.3.3V)
LED#1/DAS/DSS# (IO)(0.3.3V)

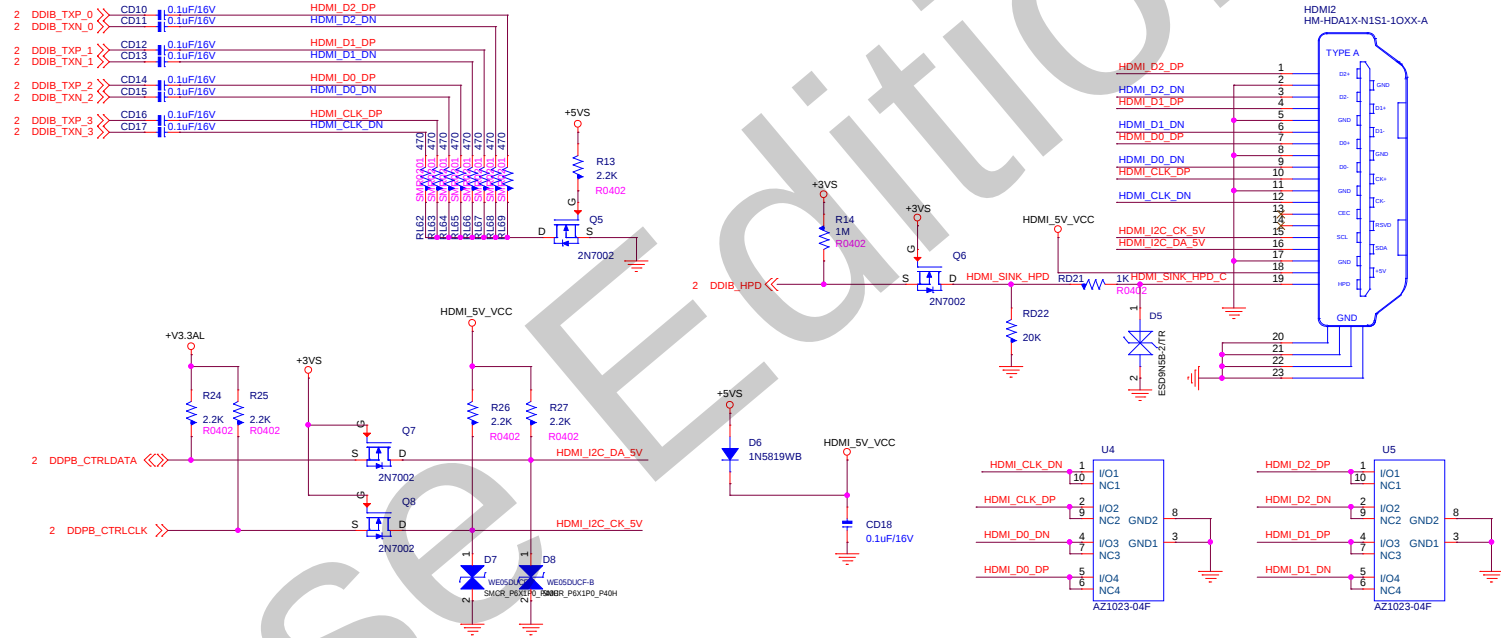
CS - Top Mechanical Key B PS - Bottom

WWAN/SSD IND (GND-WWAN/OC-SSD)
Reserved1 (Future Expansion)
Reserved2 (Future Expansion)
GND(27)
PERP1/USB3.0-RX-/SSIC-RXN
PERP1/USB3.0-RX+/SSIC-RXP
GND(33)
UIM-CLK (IO)
UIM-RESET (IO)
PETN1/USB3.0-TX-/SSIC-TXN
PETN1/USB3.0-TX+/SSIC-TXP
GND(39)
PERP0/SATA-B+
PERP0/SATA-B-
GND(45)
PETN0/SATA-A+
PETN0/SATA-A-
GND(51)
GND(53)
CLKREQ# (IO)(0.3.3V)
REFCLKP
GND(57)
ANTCTL0 (IO)(0.1.8V)
ANTCTL1 (IO)(0.1.8V)
ANTCTL2 (IO)(0.1.8V)
ANTCTL3 (IO)(0.1.8V)
Reset# (IO)(0.1.8V)
PEDET (OC-PCIe/GND-SATA)
GND(71)
GND(73)
USB3.0 IND (OC-USB3.0/GND-Other)

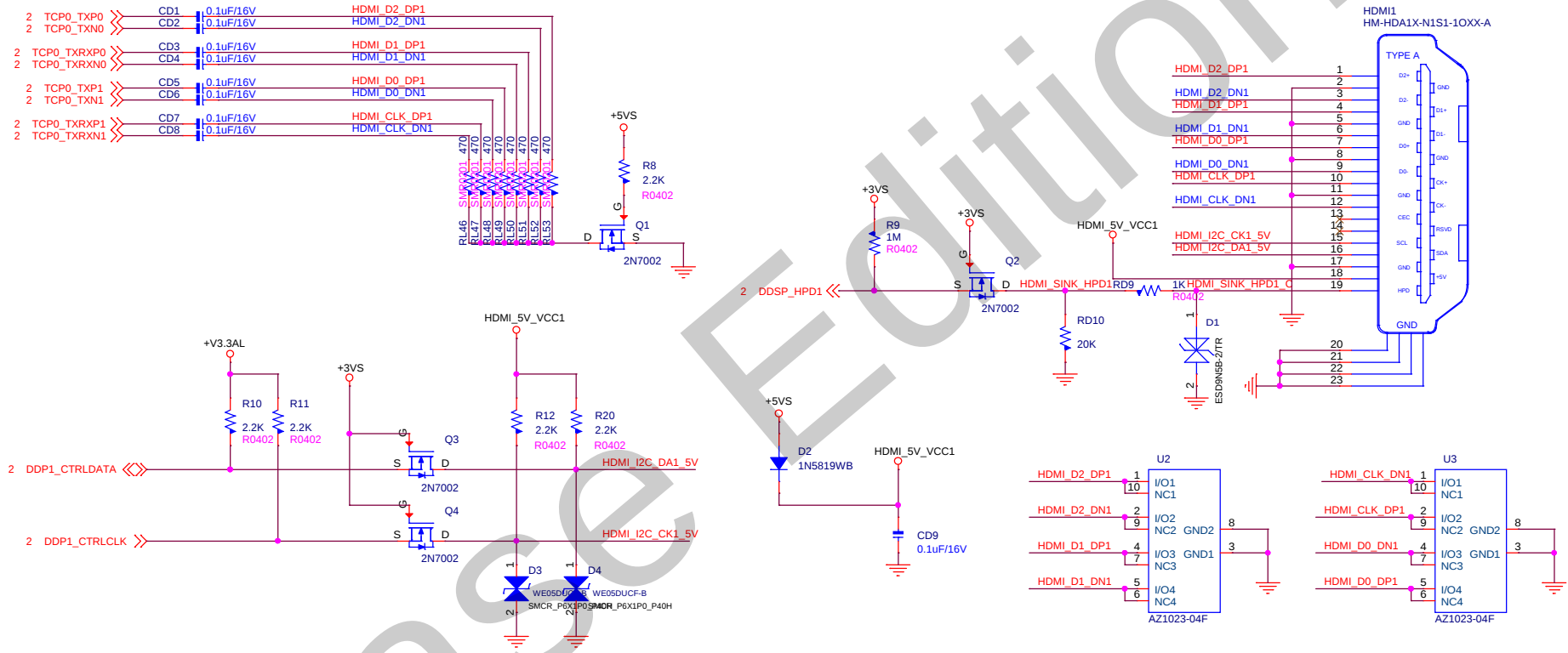
Audio0(0.1.8V)
Audio1(0.1.8V)
Audio2(0.1.8V)
Audio3(0.1.8V)
UIM-RF# (IO)
GND(32)
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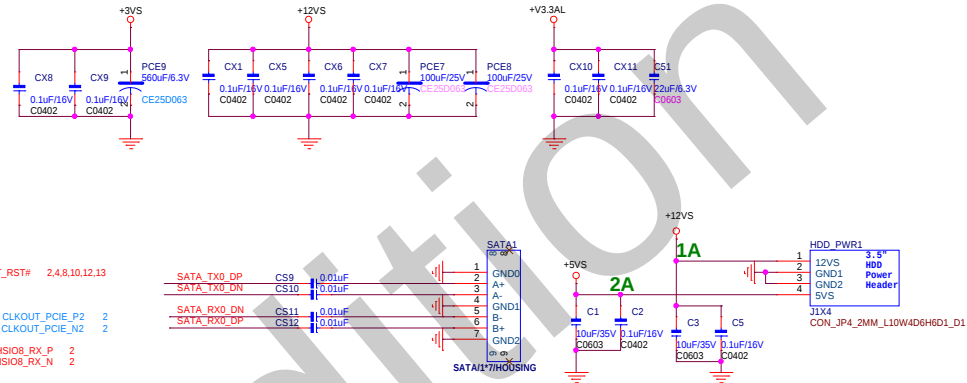
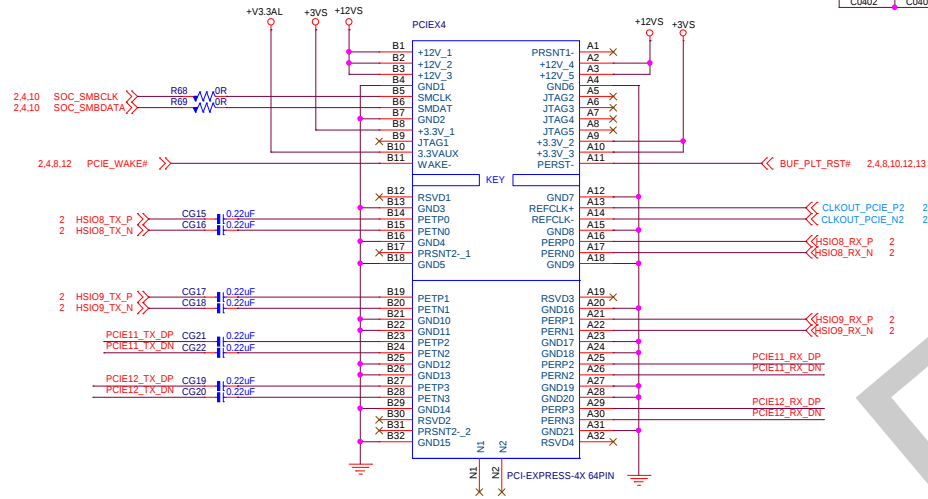
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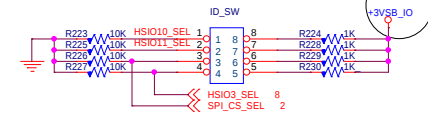
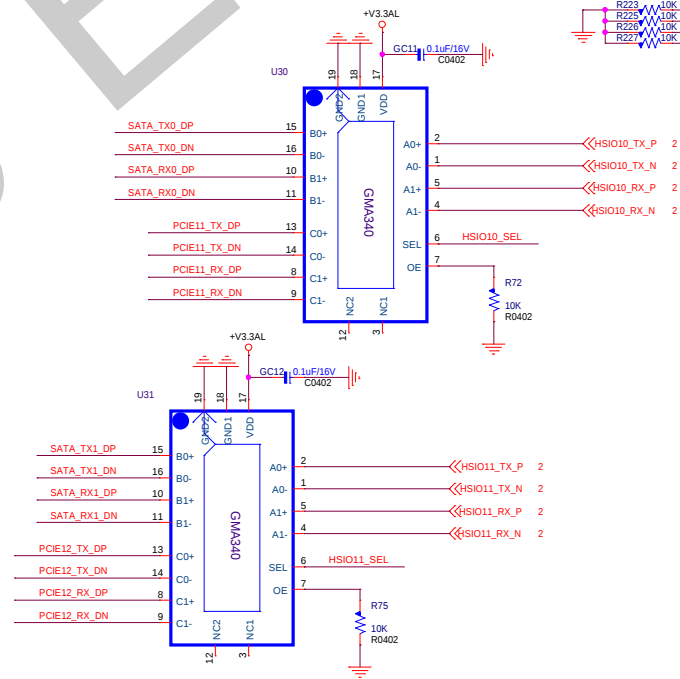
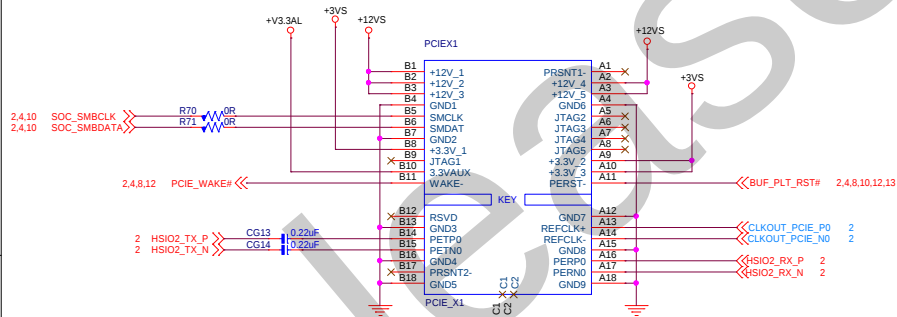
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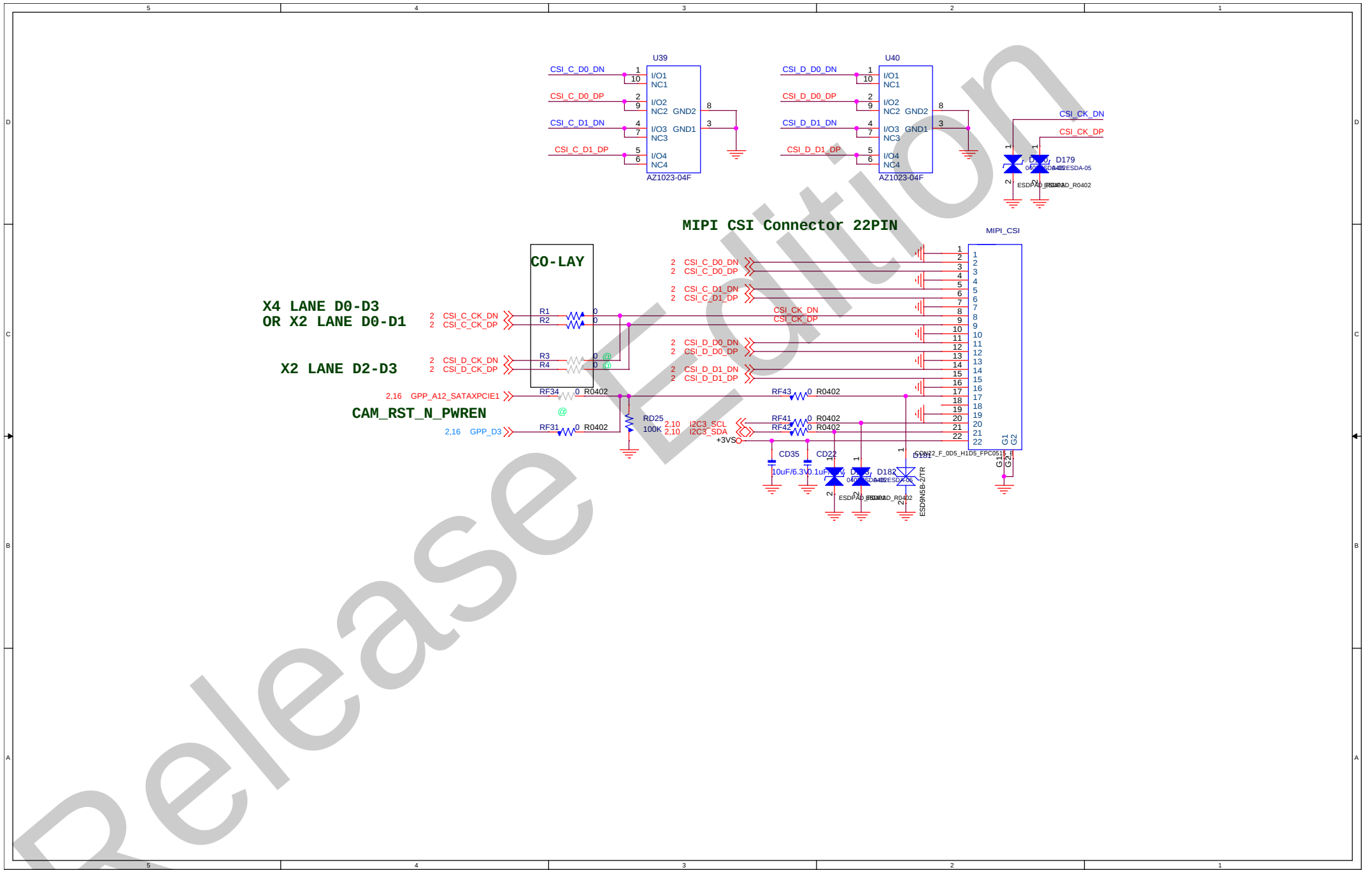


PCIEX4



PCIEX1





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