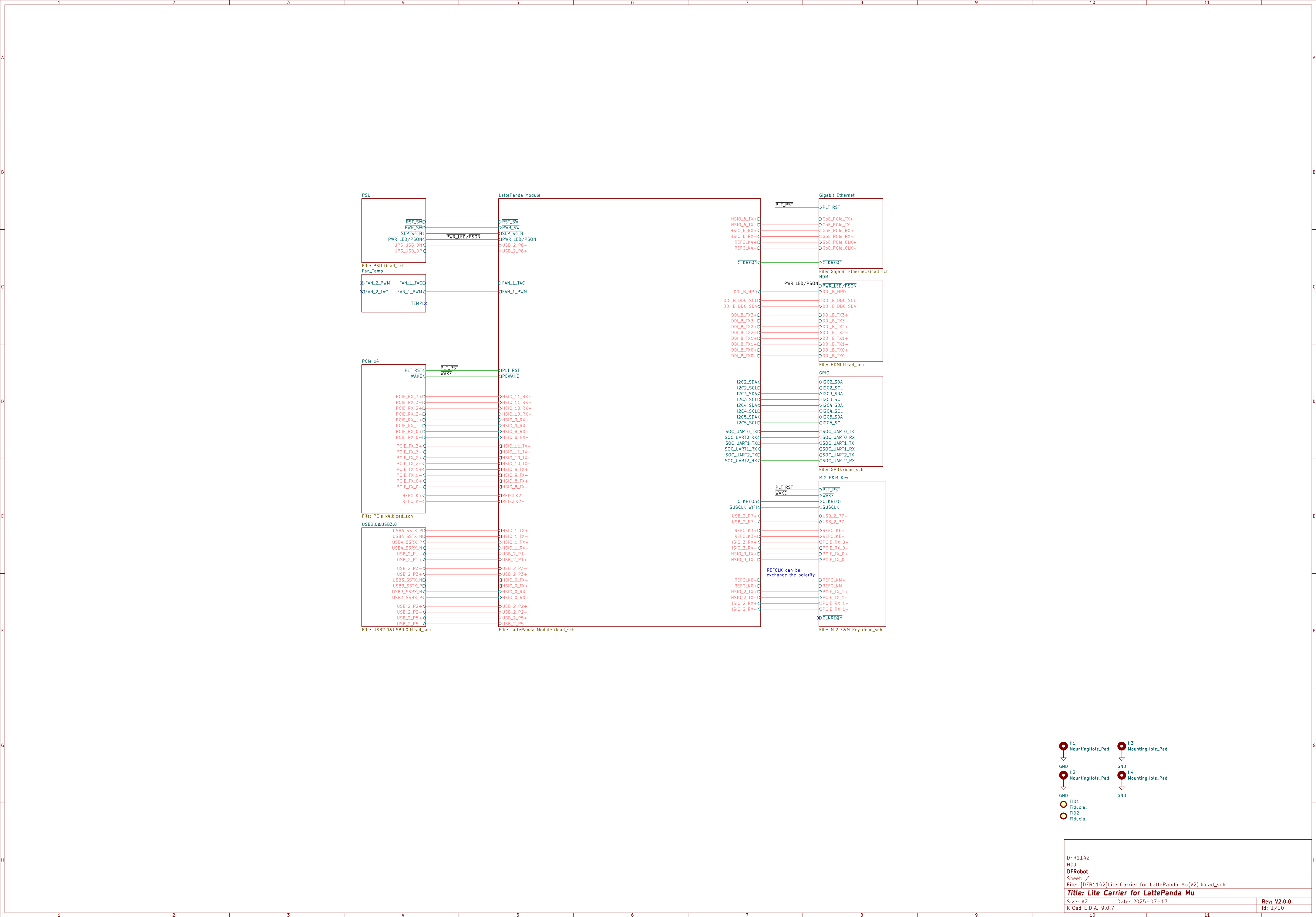
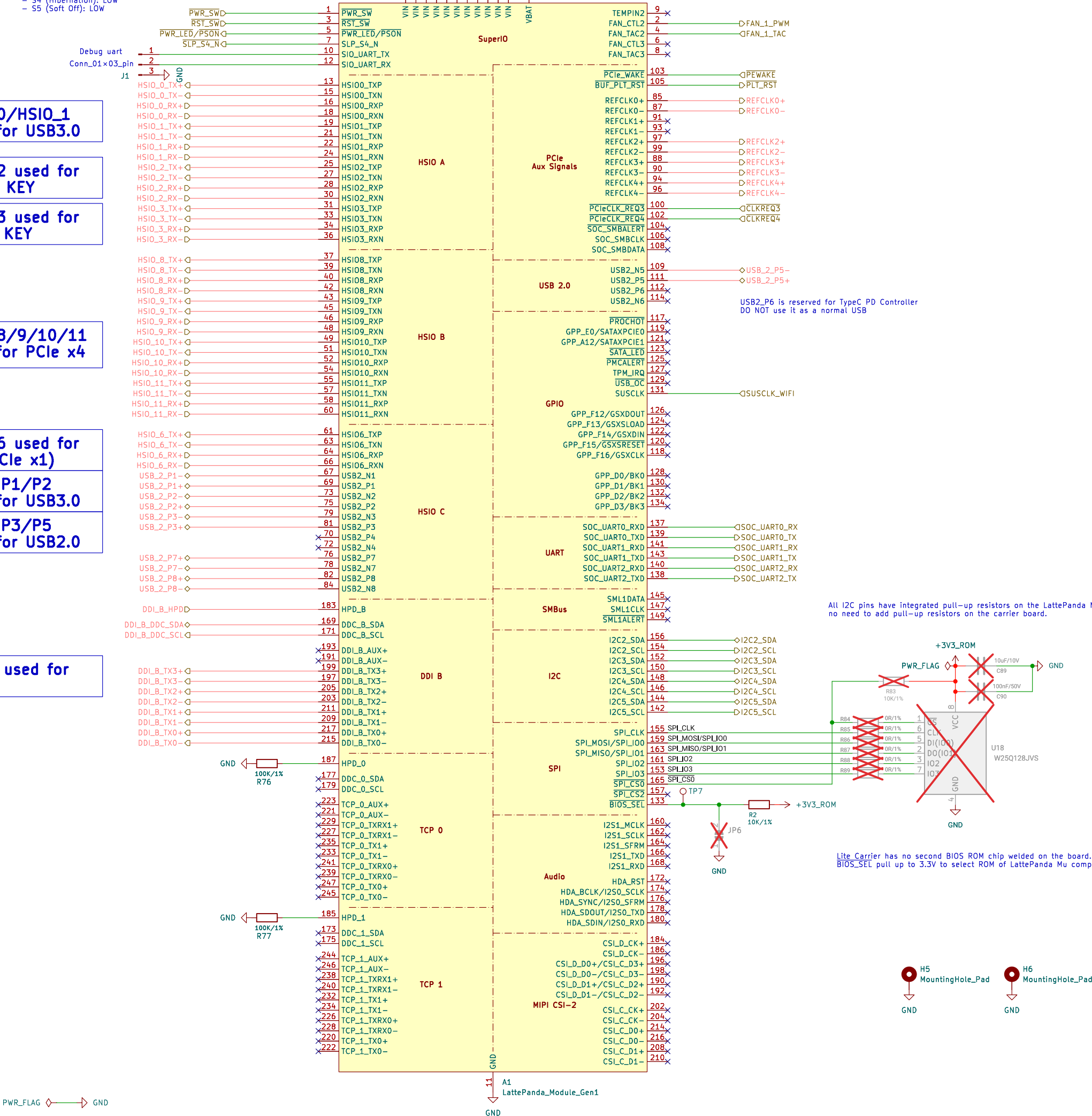




- PWR_LED/PSON:
 - S0 (Working): HIGH
 - S3 (Sleeping): LOW
 - S4 (Hibernation): LOW
 - S5 (Soft Off): LOW
- SLS_S4_N:
 - S0 (Working): HIGH
 - S3 (Sleeping): HIGH
 - S4 (Hibernation): LOW
 - S5 (Soft Off): LOW



HSIO functions are defined by the BIOS firmware,
Cannot be changed in BIOS menu.

Before starting design, you should choose one of the available BIOS configurations on:
<https://github.com/LattePandaTeam/LattePanda-Mu>
 Or contact techsupport for custom your BIOS

The default functions are defined as follows:

- HSI00: USB3.2 10Gbps
- HSI01: USB3.2 10Gbps
- HSI02: PCIe 3.0 x1
- HSI03: PCIe 3.0 x1
- HSI08: PCIe 3.0 x4 (Lane 0)
- HSI09: PCIe 3.0 x4 (Lane 1)
- HSI010: PCIe 3.0 x4 (Lane 2)
- HSI011: PCIe 3.0 x4 (Lane 3)
- HSI06: PCIe 3.0 x1

ODI/TCP functions are defined by the BIOS firmware, Cannot be changed in BIOS menu.

Before starting design, you should choose one of the available BIOS configurations on:
<https://github.com/LattePandaTeam/LattePanda-Mu>

The default functions are defined as follows:

- DDIA: eDP 1.4b (the 40Pin connector on the Mu)
- DDIB: HDMI 2.0
- TCP0: HDMI 2.0
- TCP1: USB TypeC (need external PD controller)

